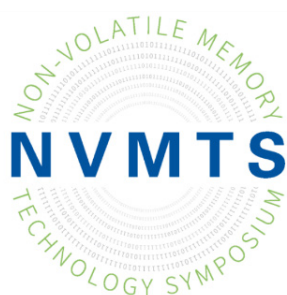


Call for Poster Abstracts. IEEE NVMTS, October 13th-15th, 2025, Tokyo, Japan



The **IEEE Non-Volatile Memory Technology Symposium (NVMTS)** is the premier international forum for exchanging advances in non-volatile memory. The symposium fosters an interactive environment for discussing the most critical developments in memory technologies, benefiting participants across the community—from graduate students to senior industry leaders.

The **24th NVMTS** will be held **October 13–15, 2026**, at the **University of Tokyo**, Tokyo, Japan. Invited experts from academia and industry will present state-of-the-art research and offer insight into future challenges for both

established and emerging memory technologies.

We are pleased to announce the **Call for Poster Abstracts**. Poster submissions are invited in all areas of non-volatile memory science and technology, including the following topics:

Topics:

1. Flash
2. MRAM
3. PCM/SOM/Selectors
4. RRAM
5. FeRAM
6. NVDRAM
7. Neuromorphic Materials and Devices
8. Memory System Technology

KEY DATES

Abstract Submission Deadline	June 30, 2026
Notification of Acceptance	July 20, 2026
Full Paper Submission Deadline	Nov. 15, 2026
Online Registration Deadline	Sept. 15, 2025

Conference Co-Chairs: Kasidit Toprasertpong, Masaharu Kobayashi, and Takuya Maeda(U of Tokyo)

Technical Program Co-Chairs: Ilya Karpov (Intel, retired, USA) and Scott Sills (Micron, USA)

NVMTS 2025 Website: <https://nvmts2026.jp/>

